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## FACSIMILE TRANSMISSION SHEET

DATE: April 21, 2008

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TO: Mr. Lewis, Certificate of Correction Branch

FAX #: 703-308-6672

FROM: Evan R. Witt

4 NUMBER OF PAGES INCLUDING COVER SHEET

### COMMENTS:

Per your request, please see the attached papers originally submitted on April 17, 2007, as well as the return receipt postcard which was date stamped April 20, 2007. Please contact me if you need any additional information.

Best regards,

Sherry Anderson  
801-321-4876

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## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P O. Box 1450, Alexandria, VA 22313-1450, on April 17, 2007

*S. Anderson*

Sherry Anderson

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No : 7,141,489 Confirmation No : 6692  
Patentee : Robert H. Burgener, II et al  
Title : FABRICATION OF P-TYPE GROUP II-VI  
SEMICONDUCTORS  
Issued : November 28, 2006  
Docket No : 14267.5  
Customer No : 21999

Commissioner for Patents  
Attn: Certificate of Correction Branch  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Request for a Revised Certificate of Correction**

Dear Sir:

Please correct the Certificate of Correction issued in connection with the above patent application as follows:

INVENTOR(S): Robert H Burgener, II et al.

A copy of the Certificate of Correction showing the correction is enclosed.

Respectfully submitted,

*E. R. Witt*

Evan R. Witt

Reg. No. 32,512

Attorney for Patentee(s)

Date: April 17, 2007  
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UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,141,489 B2

Page 1 of 1

APPLICATION NO. : 10/849347

DATED : November 28, 2006

INVENTOR(S) : Robert H. Burgener, II et al.

Robert H. Burgener, II et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 11 line 15, please delete "about 60 mm" and replace it with —about 60 nm—.

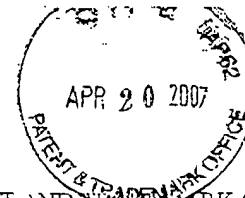


Signed and Sealed this

Tenth Day of April, 2007

JON W DUDAS

Director of the United States Patent and Trademark Office



TO THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PLEASE STAMP AND RETURN THANK YOU

Submitted:	Request for a Revised Certificate of Correction; Copy of Certificate of Correction showing correction
Inventor(s):	Robert H. Burgener et al.
Title:	FABRICATION OF P-TYPE GROUP II- VI SEMICONDUCTORS
Patent No.:	7,141,489
Issue Date:	November 28, 2006
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Mailed:	April 17, 2007